

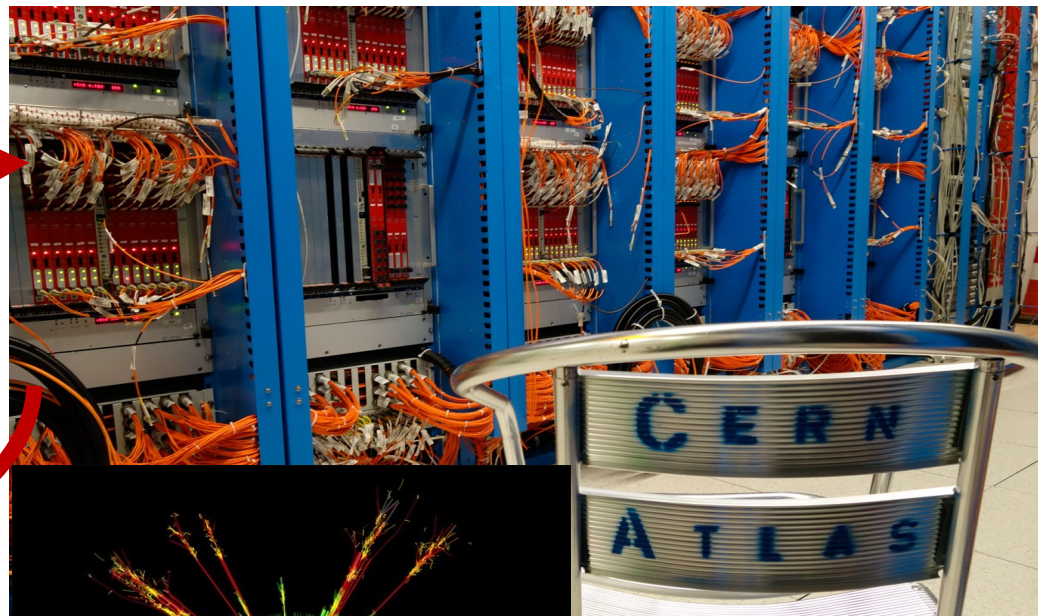
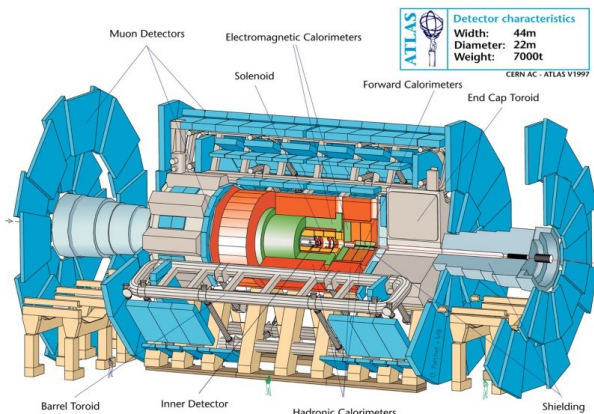


oliscience

*open logic interconnects science*

**Andrea Borga** co-founder and CEO

# FPGA - big data bulldozers



Nikhef



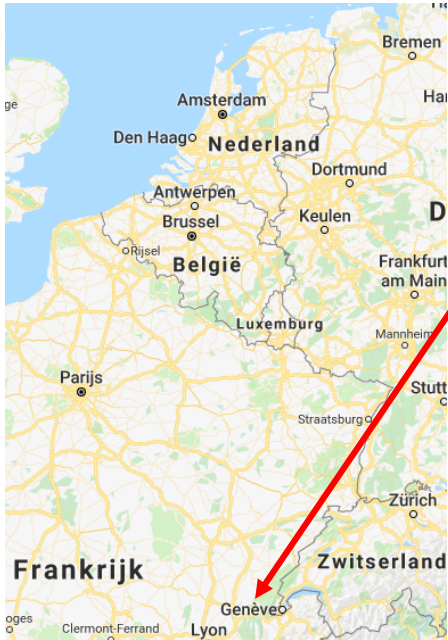
# About Oliscience BV

- Originating from the CERN-BIC at Nikhef

Nikhef



ACE



- Incubated at Amsterdam Centre for Entrepreneurship
- Based at the Amsterdam Science Park



# Oliscience management team

- **Alberto Alberton: (sales and marketing)**
  - experienced entrepreneur
  - angel investor in oliscience
- **Leo Davoli: (legal and operations)**
  - professional lawyer
  - angel investor in oliscience
- **Andrea Borga: (CEO and CTO)**
  - seasoned digital design engineer
  - passionate technologist
  - open source enthusiast
  - the geek!



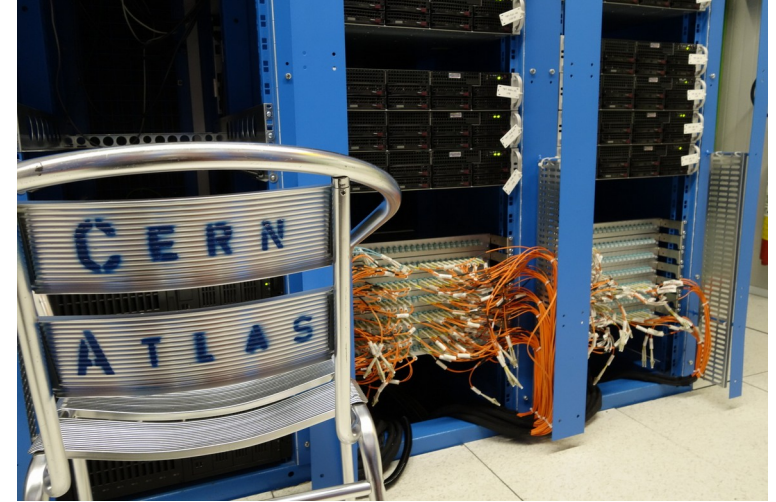
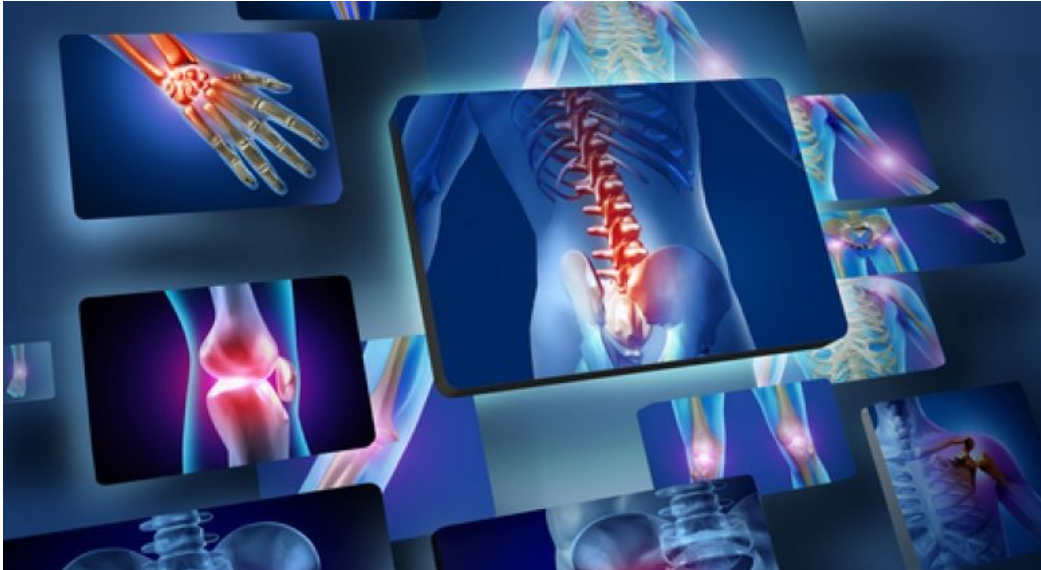
# Oliscience in a nutshell

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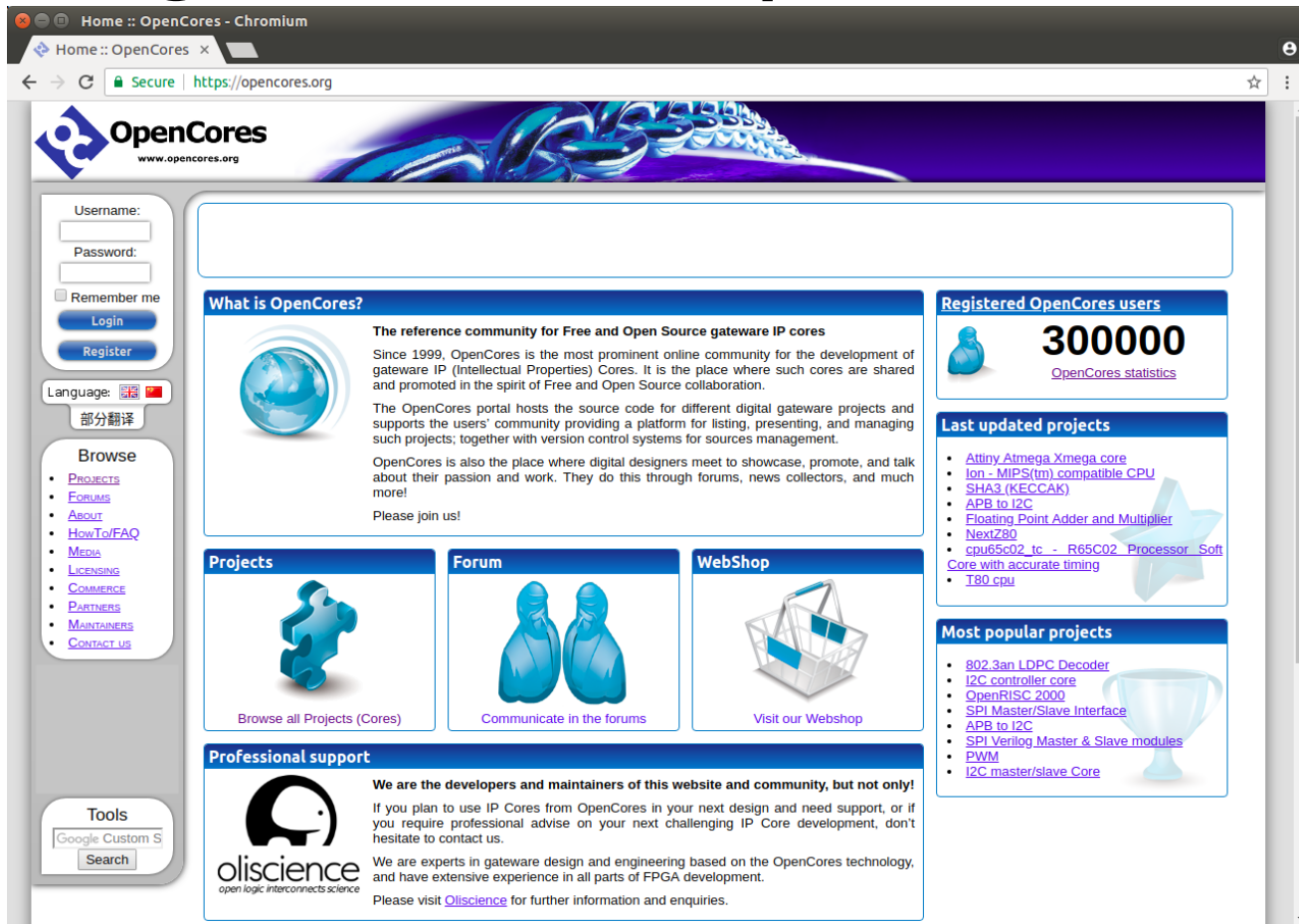
- Core business: FPGA technology
- Innovating in the field of FPGA
- Stewarding the **OpenCores.org platform** portal for the exchange of Open Source IP  
→ “we are the GitHub for FPGA”
- Providing consultancy services

# FPGA markets

- Science
- Security
- Big data
- Medical




# The “good old” OpenCores.org



The screenshot shows the OpenCores.org website in a Chromium browser window. The page features a navigation sidebar on the left with links for Projects, Forums, About, HowTo/FAQ, Media, Licensing, Commerce, Partners, Maintainers, and Contact Us. The main content area includes a login/register section, a 'What is OpenCores?' section describing the community, and several project highlights. The 'Registered OpenCores users' section shows 300,000 users. The 'Last updated projects' section lists various cores like Attiny, ARM, and T80. The 'Most popular projects' section lists cores like 802.3an, I2C, and SPI. The 'Professional support' section mentions Oliscience as the developers and maintainers.

**OpenCores**  
www.opencores.org

Username:  
Password:  
☐ Remember me  
[Login](#)  
[Register](#)

Language:  部分翻译

**Browse**

- [PROJECTS](#)
- [FORUMS](#)
- [ABOUT](#)
- [HowTo/FAQ](#)
- [MEDIA](#)
- [LICENSING](#)
- [COMMERCE](#)
- [PARTNERS](#)
- [MAINTAINERS](#)
- [CONTACT US](#)

**Tools**  
Google Custom S  
[Search](#)

**What is OpenCores?**

The reference community for Free and Open Source gateway IP cores

Since 1999, OpenCores is the most prominent online community for the development of gateway IP (Intellectual Properties) Cores. It is the place where such cores are shared and promoted in the spirit of Free and Open Source collaboration.

The OpenCores portal hosts the source code for different digital gateway projects and supports the users' community providing a platform for listing, presenting, and managing such projects; together with version control systems for sources management.

OpenCores is also the place where digital designers meet to showcase, promote, and talk about their passion and work. They do this through forums, news collectors, and much more!

Please join us!

**Registered OpenCores users**

 **300000**  
[OpenCores statistics](#)


**Last updated projects**


- [Attiny Atmega Xmega core](#)
- [lon - MIPS\(tm\) compatible CPU](#)
- [SHA3 \(KECCAK\)](#)
- [APB to I2C](#)
- [Floating Point Adder and Multiplier](#)
- [NextZ80](#)
- [cpu65c02\\_ic - R65C02 Processor Soft Core with accurate timing](#)
- [T80 cpu](#)

**Most popular projects**


- [802.3an LDPC Decoder](#)
- [I2C controller core](#)
- [OpenRISC 2000](#)
- [SPI Master/Slave Interface](#)
- [APB to I2C](#)
- [SPI Verilog Master & Slave modules](#)
- [PWM](#)
- [I2C master/slave Core](#)

**Projects**  
  
[Browse all Projects \(Cores\)](#)

**Forum**  
  
[Communicate in the forums](#)

**WebShop**  
  
[Visit our Webshop](#)

**Professional support**

  
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We are the developers and maintainers of this website and community, but not only!

If you plan to use IP Cores from OpenCores in your next design and need support, or if you require professional advise on your next challenging IP Core development, don't hesitate to contact us.

We are experts in gateway design and engineering based on the OpenCores technology, and have extensive experience in all parts of FPGA development.

Please visit [Oliscience](#) for further information and enquiries.



# Premium partner

## ASTRON

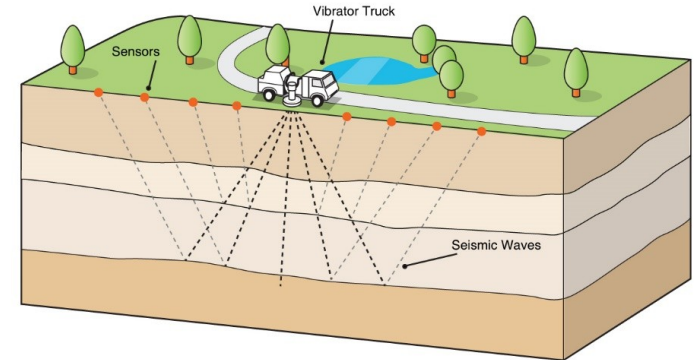
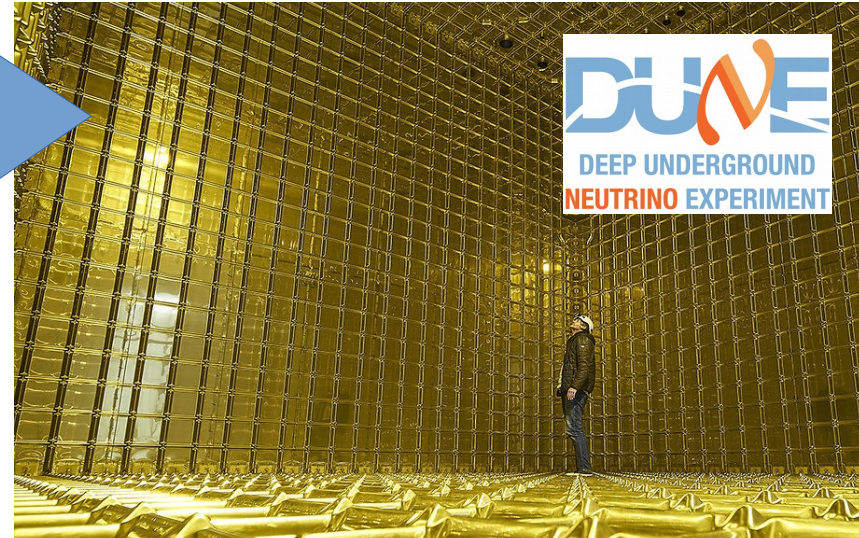
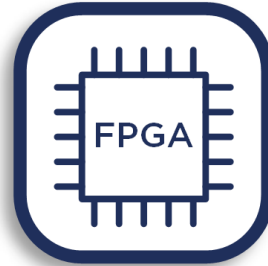
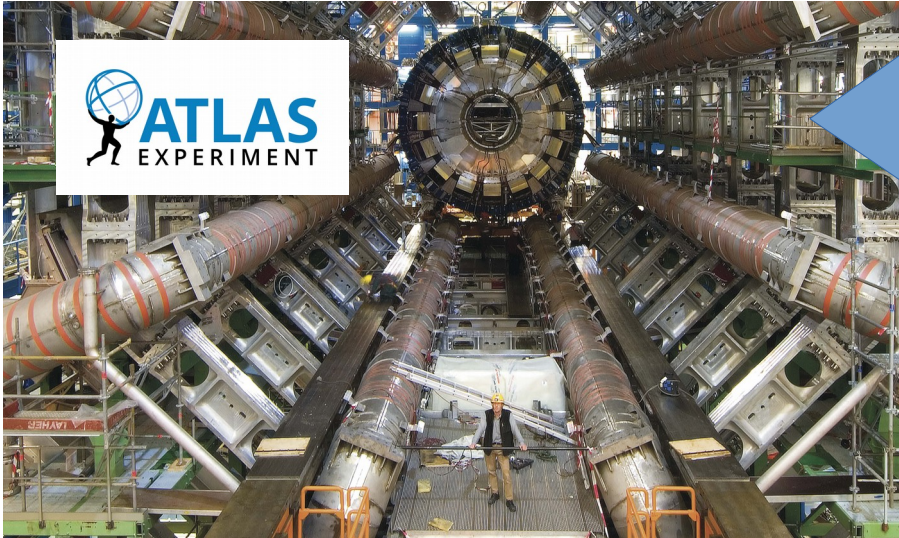
Netherlands Institute for Radio Astronomy

**“[...] We are working on the opposite extremes of physics, but we are using the same technology.** This collaboration allows us to share ideas and reuse FPGA designs, which will help to speed-up the process of engineering the tools for science.”- *Daniel Van der Schuur*





# Consultancy customer

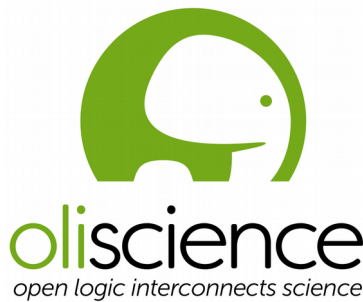


# Challenge

- we are making speed on the runway but...
- **...we need to take-off!**
- Business Development
  - building a sales funnel
  - push marketing
- Tackle technology challenges
  - modernize platform
- Explore new ways to collaborate and grow



# Thank you



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**OpenCores**  
www.opencores.org

[www.opencores.org](http://www.opencores.org)  
[www.oliscience.nl](http://www.oliscience.nl)

**LinkedIn:** <https://www.linkedin.com/company/oliscience/>

**Twitter:** @Oliscience101