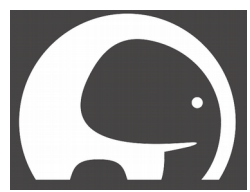




oliscience

*open logic interconnects science*

Andrea Borga (co-founder, CEO and CTO)



# Oliscience

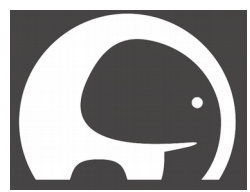
---

- Young and passionate startup of committed professionals
- Originating from the CERN@Nikhef BIC (Business Incubator Centre)



- Coached by the Amsterdam Centre for Entrepreneurship
- Based at the Amsterdam Science Park



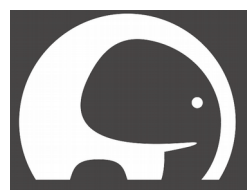


# Oliscience team

---

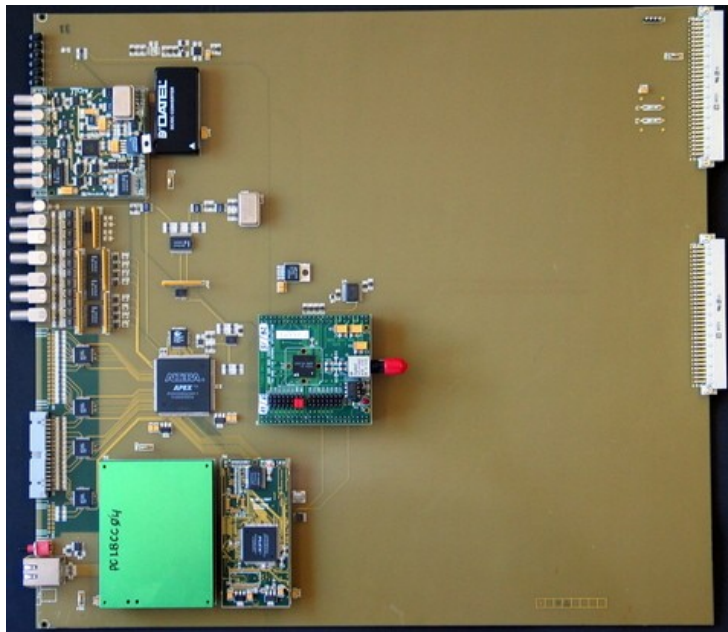
- Alberto Alberton: (sales and marketing)
  - experienced entrepreneur  
(owns company in electronics hardware manufacturing)
- Leo Davoli: (legal and operations)
  - professional lawyer
- Andrea Borga: (CEO and CTO)
  - seasoned digital design engineer
  - passionate technologist
  - open source enthusiast



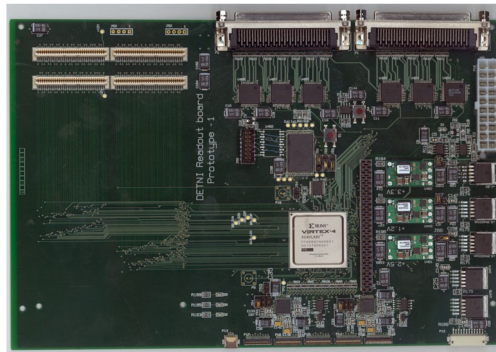


# About me

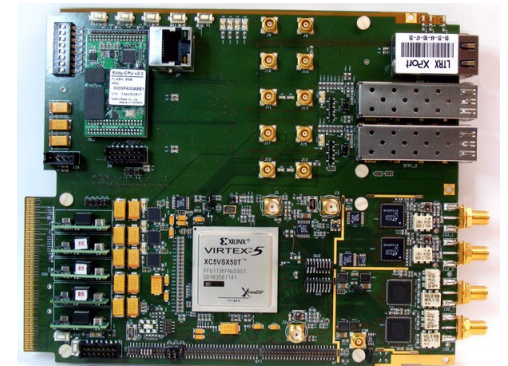
- In the engineering world most commonly known as a “Digital Designer” or... “the FPGA guy”



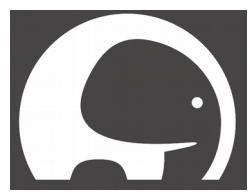
**CERN – LHCb (2004)**



**FZ-Juelich – DETNI (2007) Elettra – FERMI@Elettra (2011)**



**Nikhef – ATLAS (today)**



# Recurring observations

---

- In all the places I've been designing electronics for scientific purposes...
- In many academic and technical trainings I've been coaching or attending...
- There was and there is a recurring mention to a place on the web where designers can learn, publish, share, use and reuse code for FPGAs...






# What empowers experts



Home :: OpenCores - Chromium

Home :: OpenCores x oliscience - open lo x

Secure | <https://opencores.org>

 **OpenCores**  
www.opencores.org

Username:  
Password:  
☐ Remember me  
[Login](#)  
[Register](#)

Language:    
部分翻译


**Browse**

- [PROJECTS](#)
- [FORUMS](#)
- [ABOUT](#)
- [HowTo/FAQ](#)
- [MEDIA](#)
- [LICENSING](#)
- [COMMERCE](#)
- [PARTNERS](#)
- [MAINTAINERS](#)
- [CONTACT US](#)

**Tools**

Google Custom S  
[Search](#)

**What is OpenCores?**



**The reference community for Free and Open Source gateway IP cores**


Since 1999, OpenCores is the most prominent online community for the development of gateway IP (Intellectual Properties) Cores. It is the place where such cores are shared and promoted in the spirit of Free and Open Source collaboration.

The OpenCores portal hosts the source code for different digital gateway projects and supports the users' community providing a platform for listing, presenting, and managing such projects; together with version control systems for sources management.

OpenCores is also the place where digital designers meet to showcase, promote, and talk about their passion and work. They do this through forums, news collectors, and much more!

Please join us!

**Registered OpenCores users**

 **284381**  
[OpenCores statistics](#)


**Last updated projects**

- [Register Oriented Instruction Sets](#)
- [LEM1\\_9](#)
- [USB 1.1 Function IP Core](#)
- [2's compliment adder subtractor](#)
- [SpaceWireSystemC](#)
- [ODESS Multicore Project](#)
- [Video Stream Scaler](#)
- [Amber ARM-compatible core](#)


**Most popular projects**

- [CF Reconfigurable Computing Array](#)
- [I2C controller core](#)
- [Hilbert Transformer](#)
- [SPI Master/Slave Interface](#)
- [Raggedstone PCI Spartan-3 board](#)
- [SPI Verilog Master & Slave modules](#)
- [double\\_fpu\\_verilog](#)
- [I2C master/slave Core](#)


**Projects**

  
[Browse all Projects \(Cores\)](#)


**Forum**

  
[Communicate in the forums](#)

**WebShop**

  
[Visit our Webshop](#)

**Professional support**

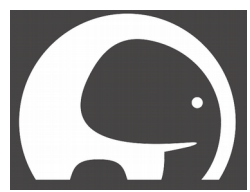


**We are the developers and maintainers of this website and community, but not only!**

If you plan to use IP Cores from OpenCores in your next design and need support, or if you require professional advise on your next challenging IP Core development, don't hesitate to contact us.

We are experts in gateway design and engineering based on the OpenCores technology, and have extensive experience in all parts of FPGA development.

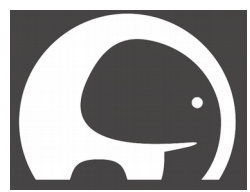
Please visit [Oliscience](#) for further information and enquiries.



# OpenCores.org

---

- Funded in 1999 by a Slovenian student
- A portal that since then collected ~300.000 professionals in the field of gateware IPs
- Generates ~500.000 views per month
- Strong identity, established trademark, consistent community, very specialized, potentially business oriented
- Used to be maintained by a Swedish company
- Unkempt since 2012, **was acquired by Oliscience in 2017**



# Our mission

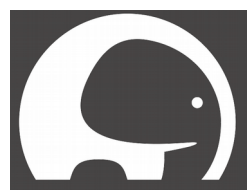
---

**we develop, drive and promote  
the OpenCores portal and community**

- Research institutions
- Universities
- High-tech corporates

**engage designers on our portal  
and foster common practices**

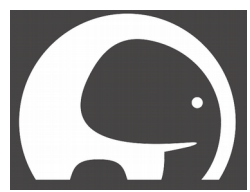




# Work done so far

---

- Ported the website to a Dutch hosting company (2 servers on TransIP)
- Stabilized the subversion back-end and boosted its performance
- Reviewed the whole site static content
- Cleaned up the project listing from empty or obviously useless projects
- Added basic new features, e.g. user project starring, better browsing and user experience



# Our ambition

---

- Reactivate the community and **take OpenCores to the next level**
- The portfolio of IP Cores shared is quite extensive:
  - many micro processors architectures (ISA)
  - virtually all standard peripherals
  - a lot of general infrastructure cores
  - **stimulate the community to publish new content**
- Improve the IP Cores **content quality**
- **Engage** other research institutions and universities to support our work and contribute



# ASTRON partnership



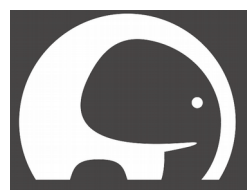
**ASTRON**

Netherlands Institute for Radio Astronomy

03-08-2018

CERN KT / FOSSi workshop

[andy@oliscience.nl](mailto:andy@oliscience.nl)



---

# Thank you



[www.opencores.org](http://www.opencores.org)

[www.oliscience.nl](http://www.oliscience.nl)

LinkedIn: <https://www.linkedin.com/company/oliscience/>

Twitter: @Oliscience101