

# Oliscience

**what's next?**

by

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presently Electronics Technology department

# The what and the why?

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- oscilloscope with “metrics” and...
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# The what and the why?

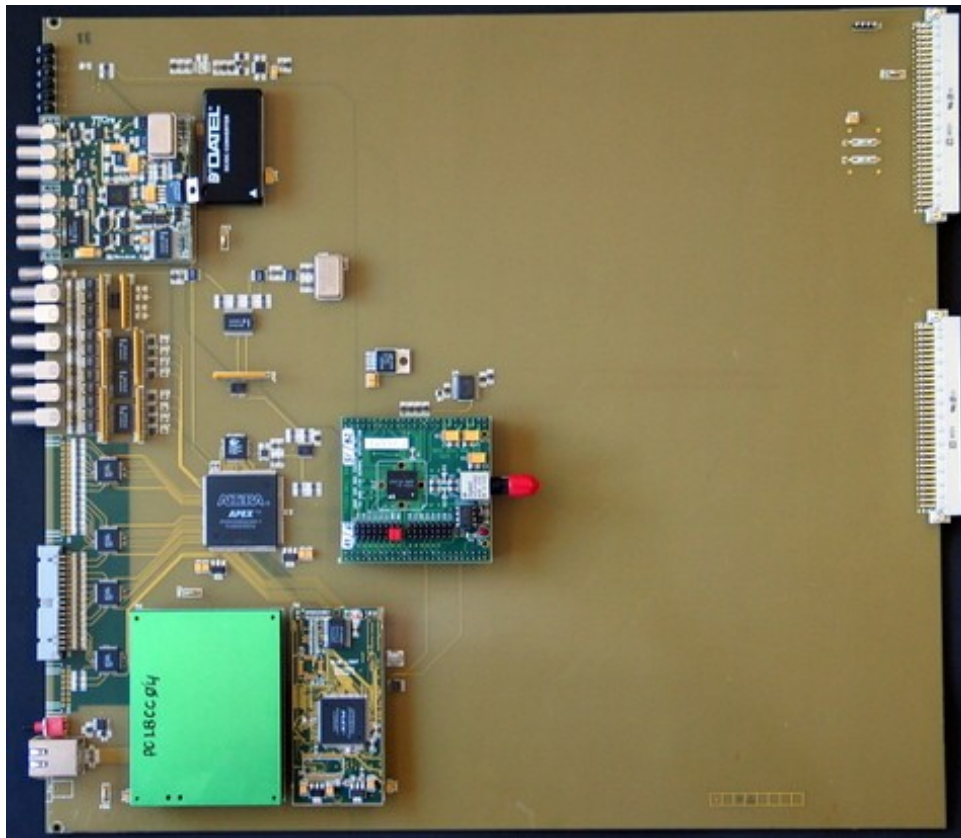
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- **Disclaimer:** if you go from engineering to business you query-replace the words...
- oscilloscope with “metrics” and...
- measurement with “impact”...
- and you’re good to go...
- However... if you are a problem solver:
- you tend to like solving problems all the time...
- even in “business”, no matter definitions and tools!

# Who am I

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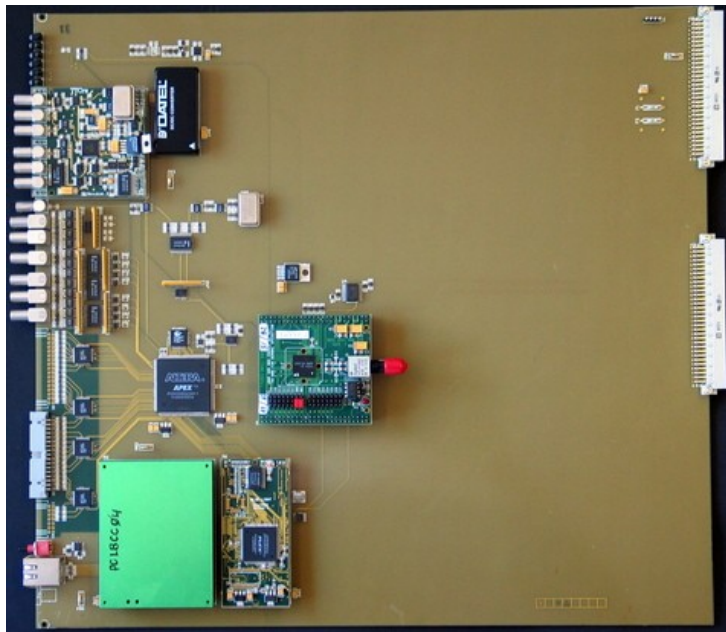
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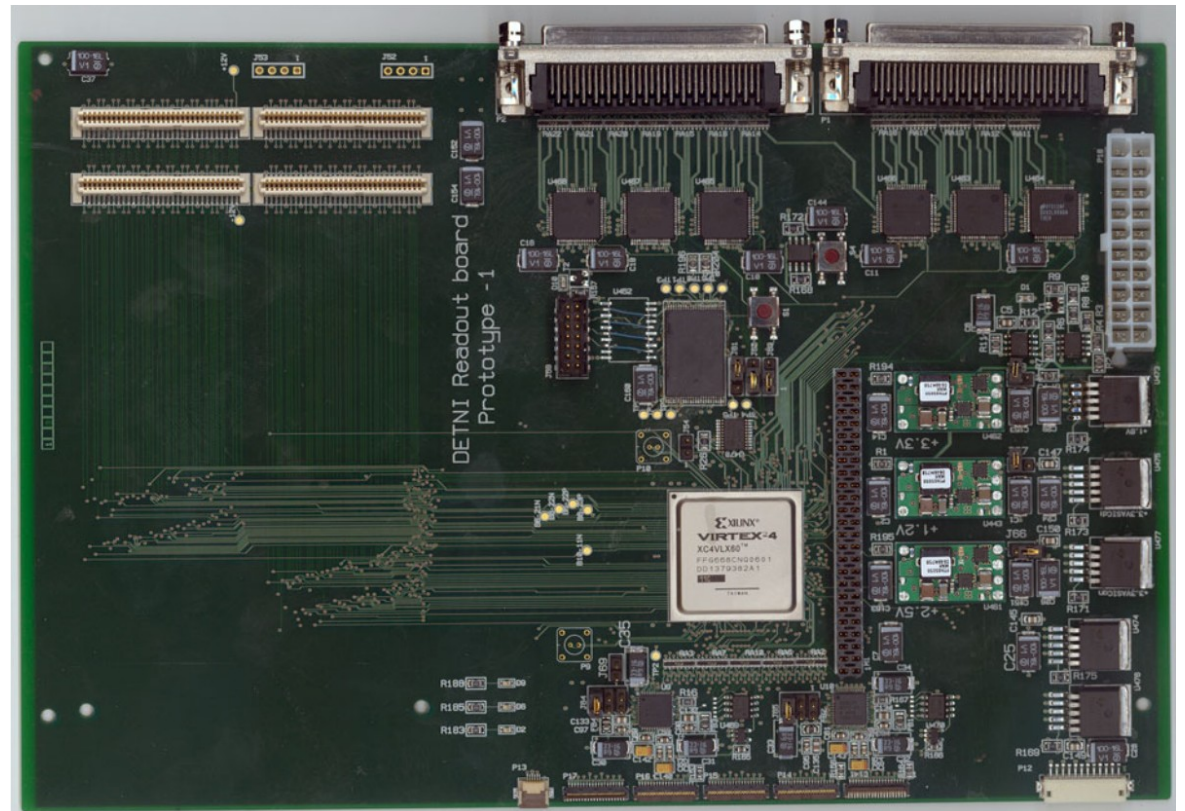
**CERN – LHCb (2004)**

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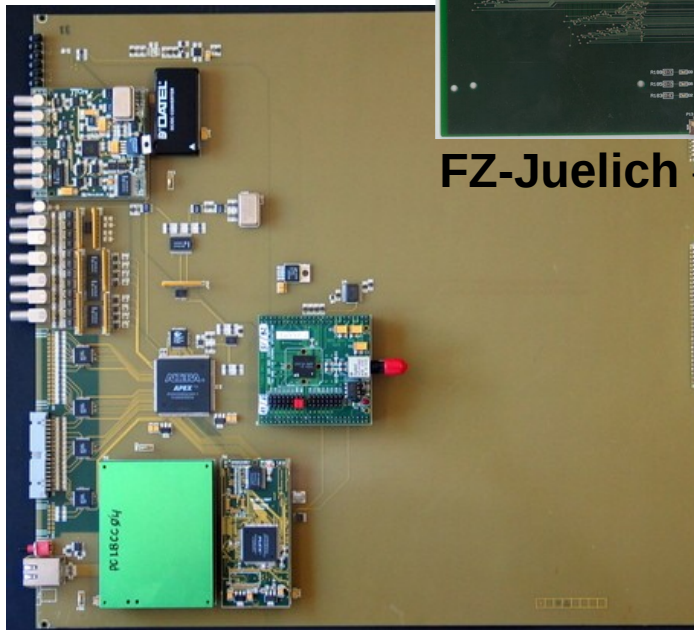


**FZ-Juelich – DETNI (2007)**

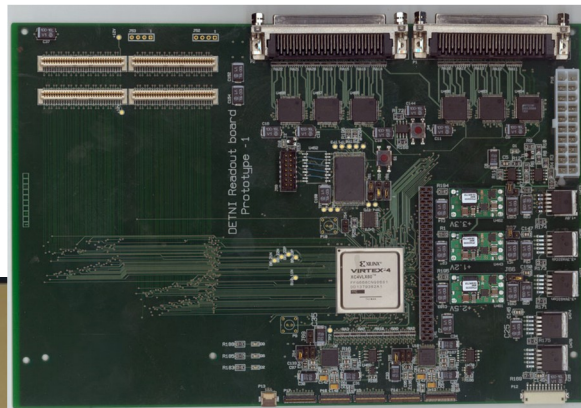


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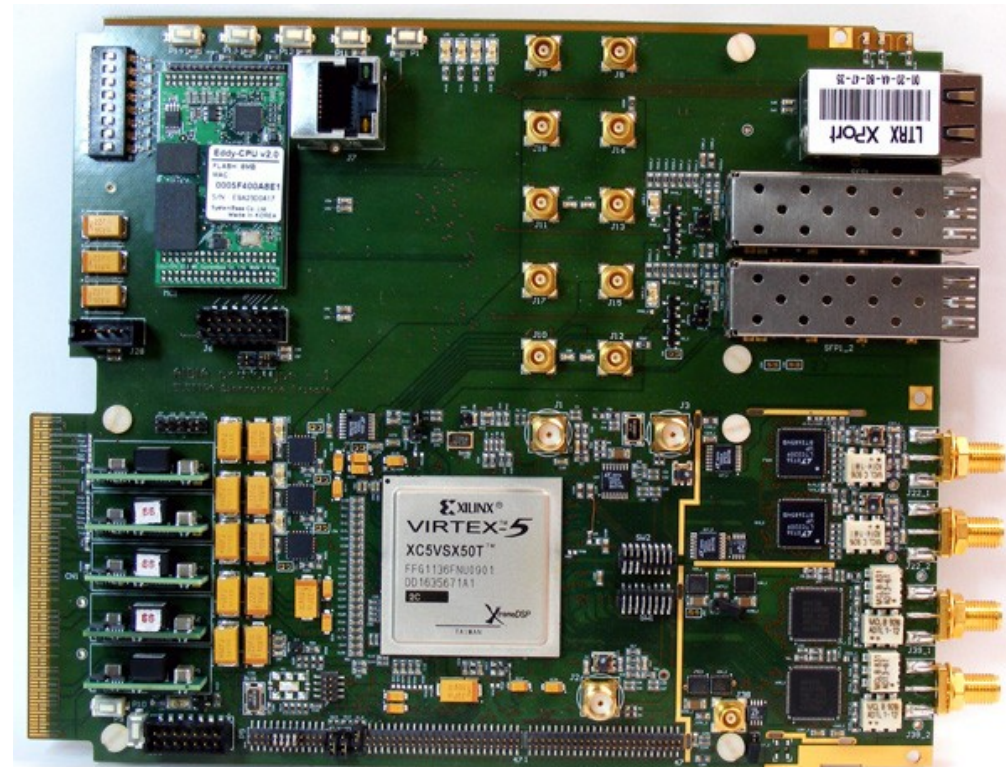
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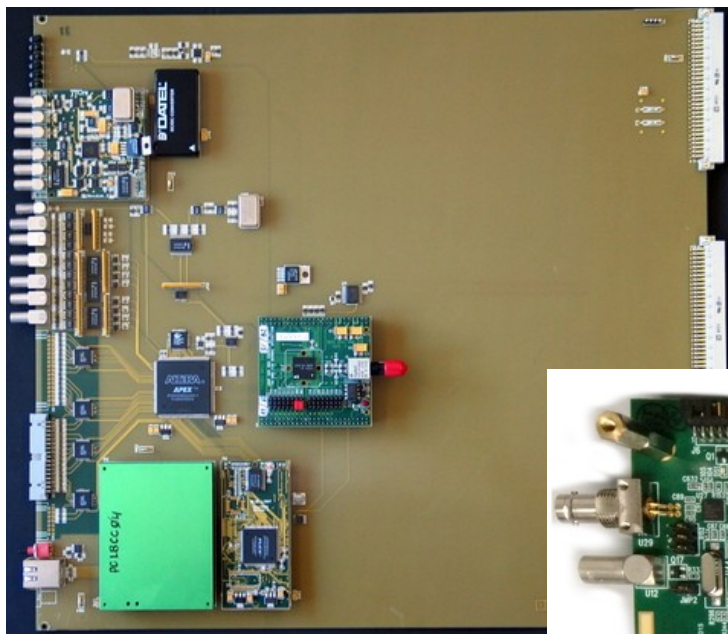


**Elettra – Fermi@Elettra (2011)**

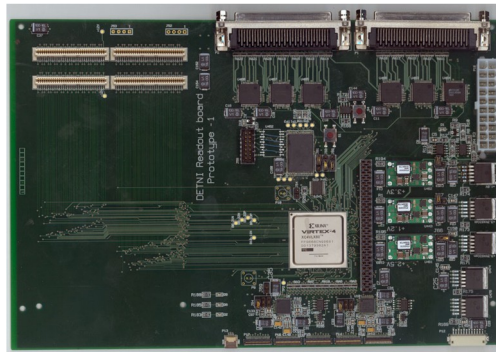


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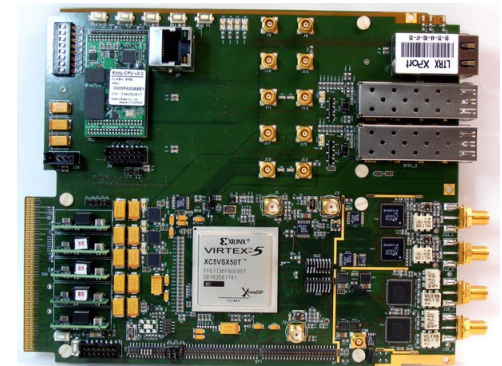
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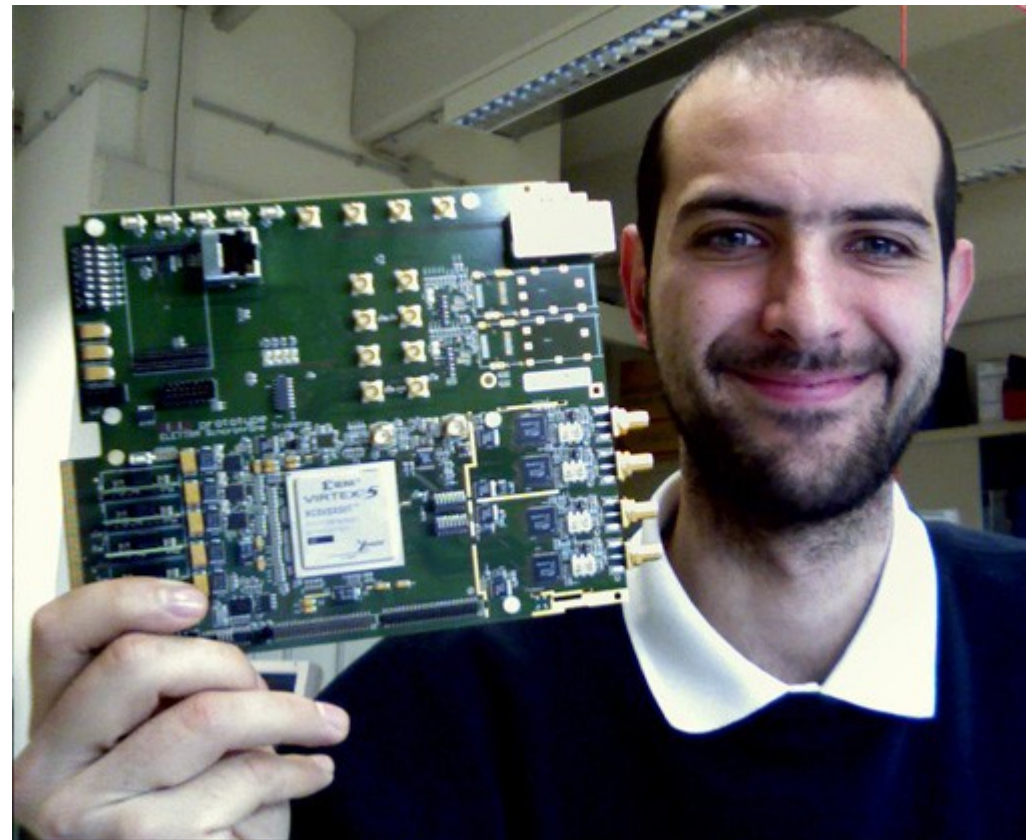
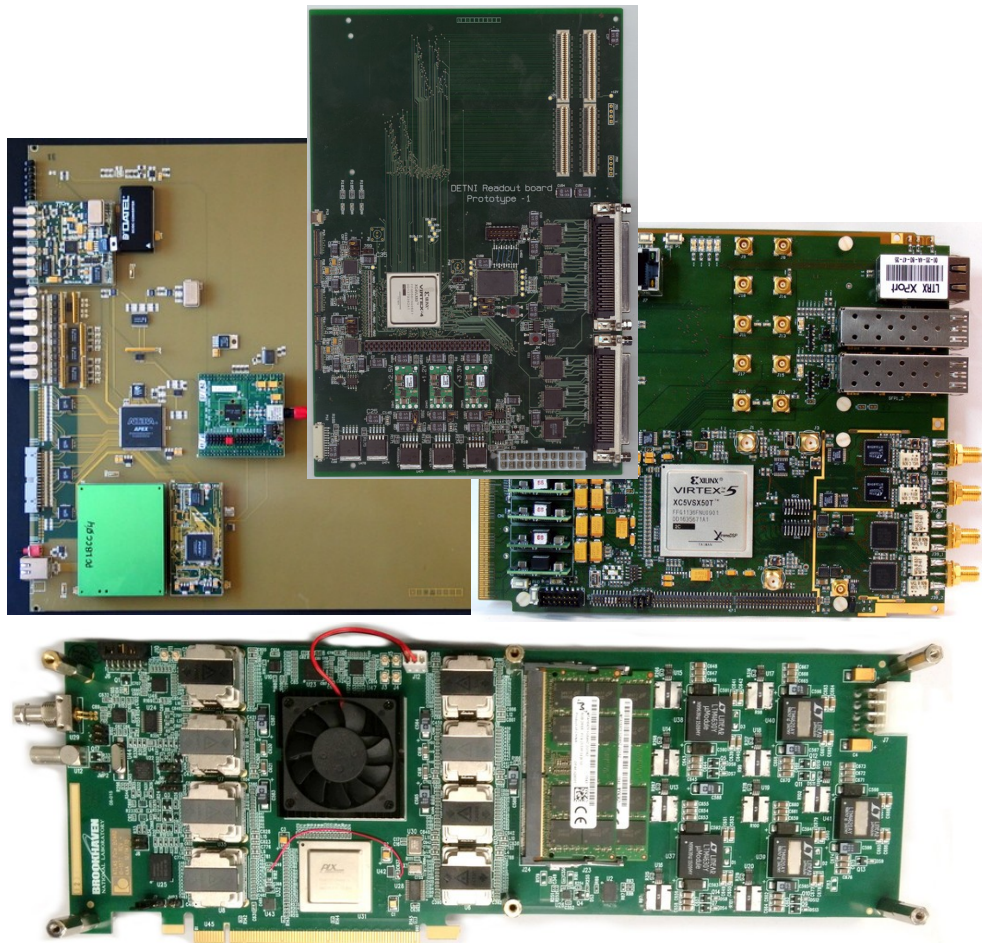


**Nikhef – ATLAS (today)**



# What I see...

- There is a problem inside...

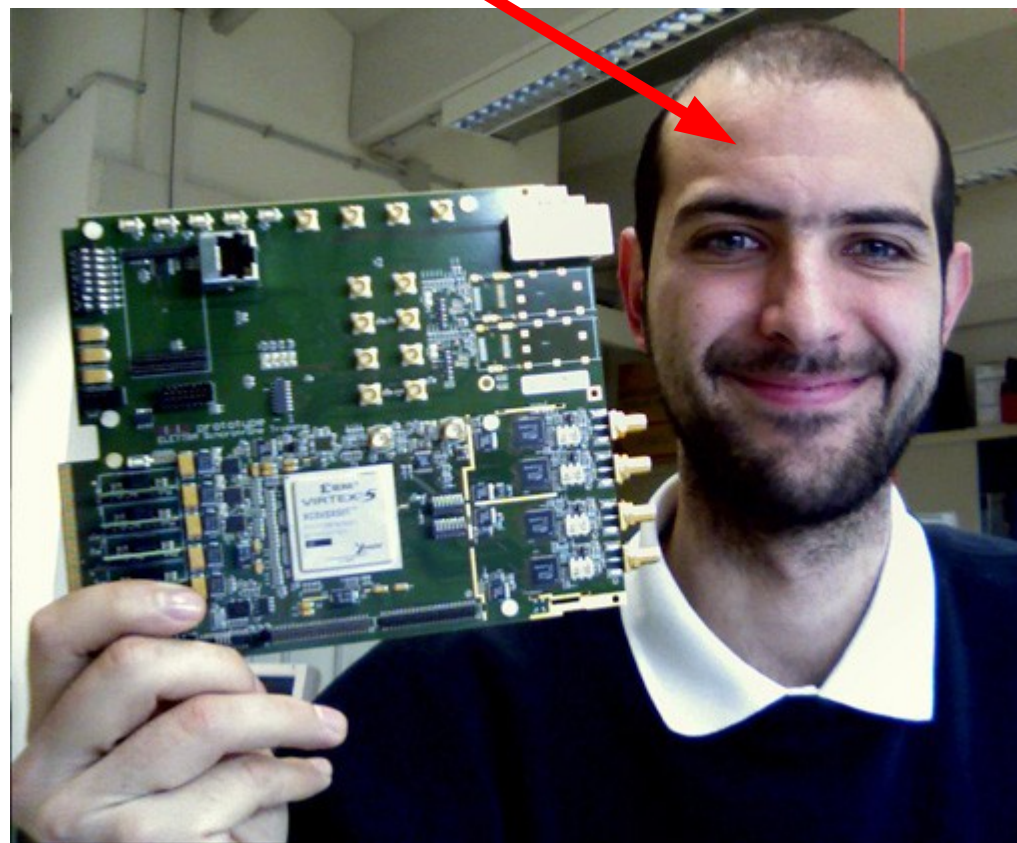
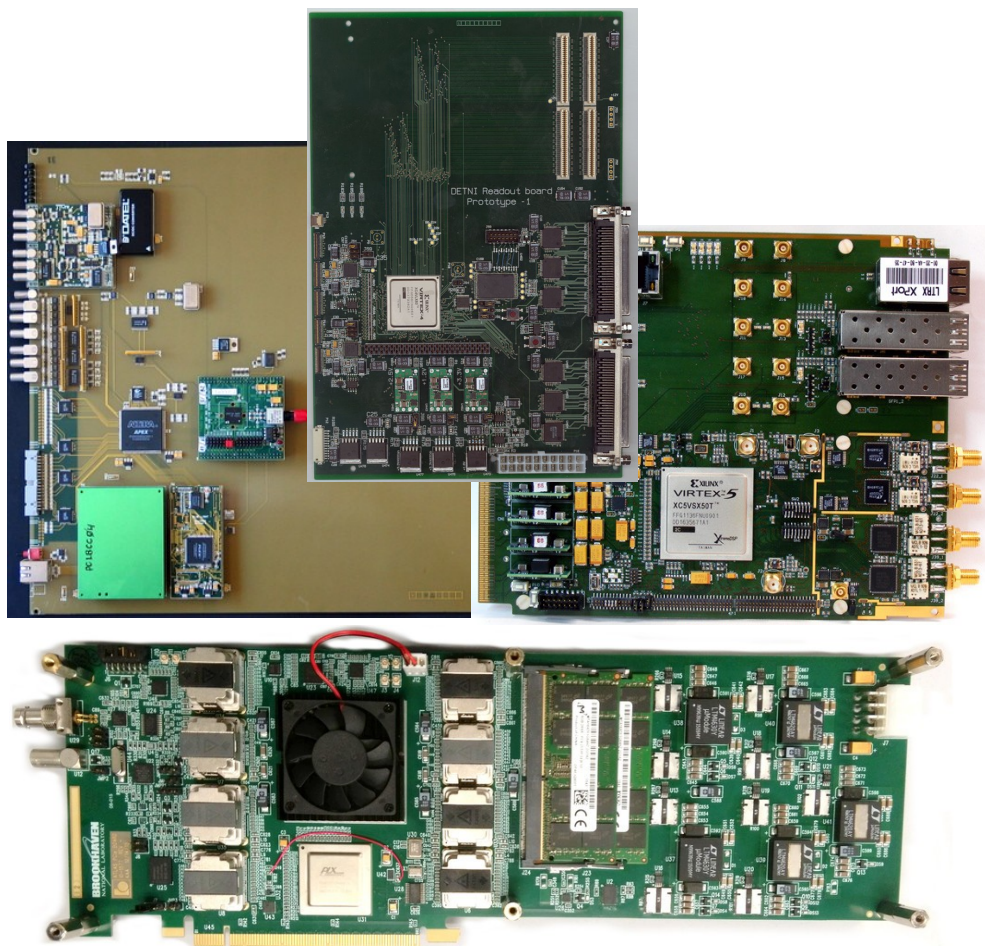




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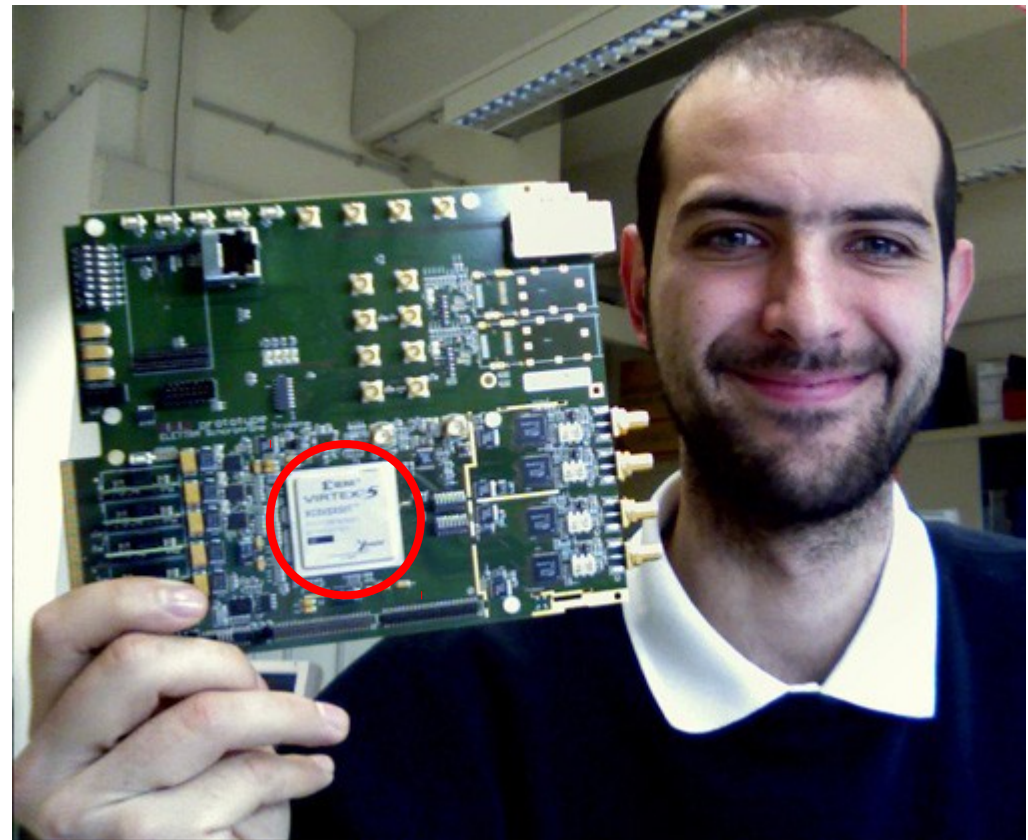
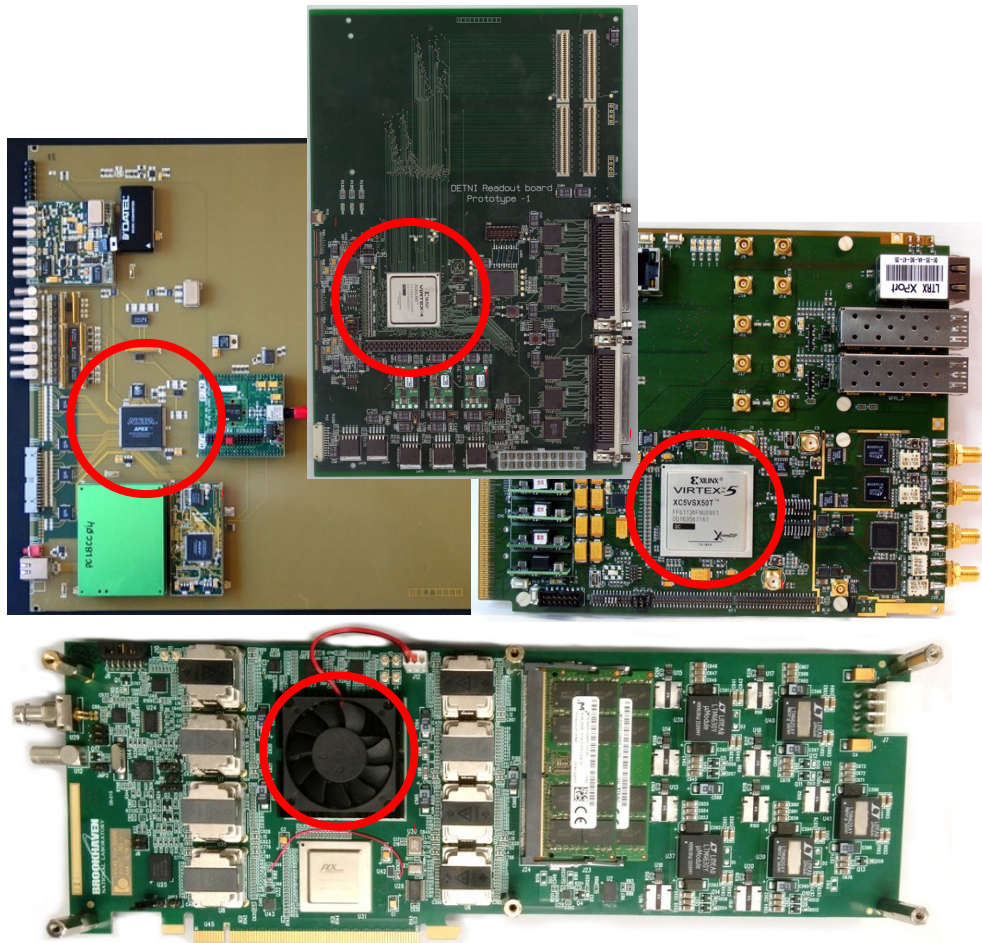
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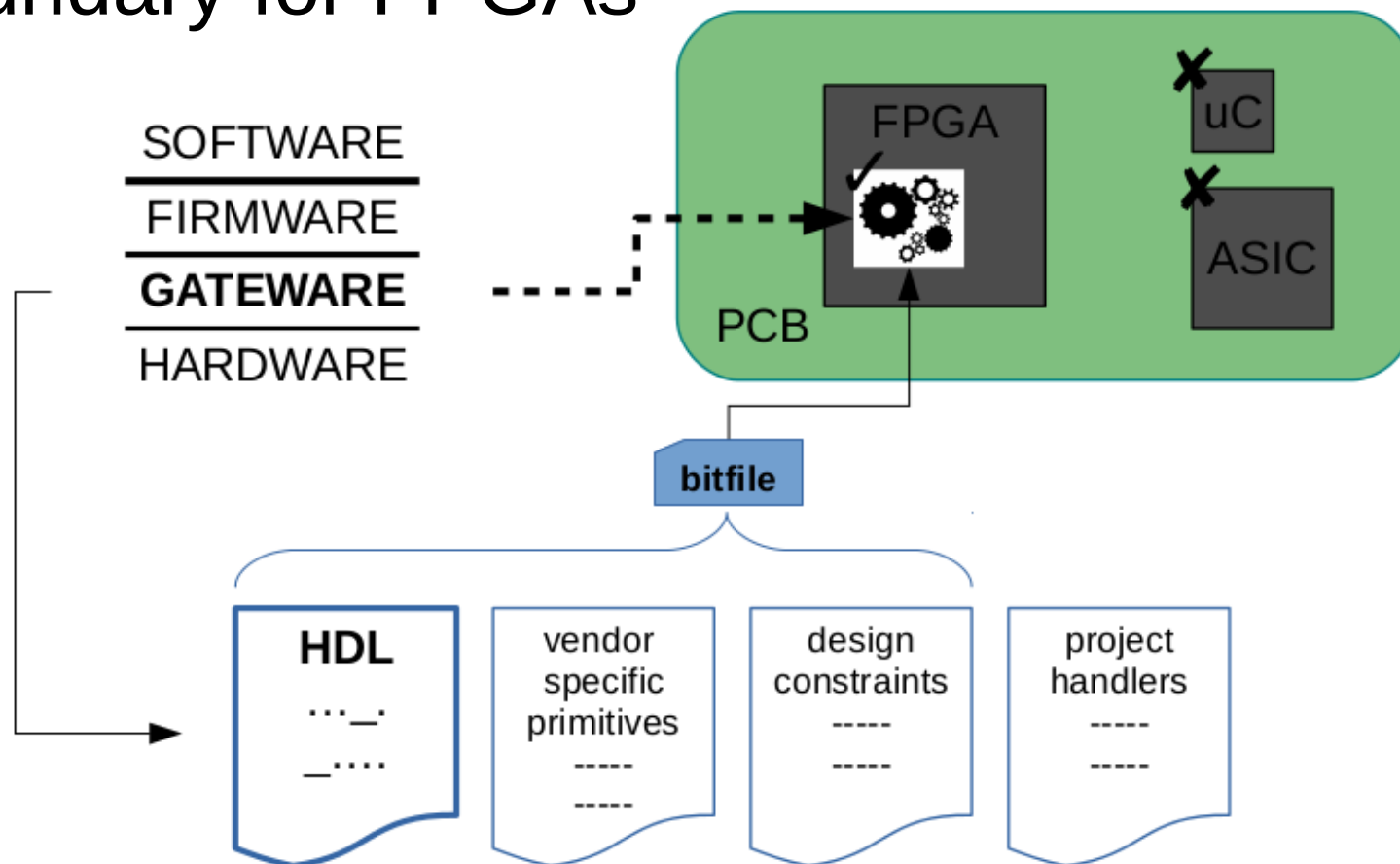
# The problems

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- There is no solid definition of **gateway** and its boundary for FPGAs
- There is a lot of effort duplication especially in our scientific community
- There is no solid metrics to measure the strength of digital designers
- There are opportunities for companies operating in an open source field applied to gateway for FPGAs

# The problems

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# The problems

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- There is a lot of effort duplication especially in our scientific community
- There is no solid metrics to measure the strength of digital designers
- There are opportunities for companies operating in an open source field applied to gateware for FPGAs
- **... where to start fixing those?**



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Andrea.

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### What is OpenCores?



**the #1 community within open source hardware IP-cores**

OpenCores is the world's largest site/community for development of hardware IP cores as open source.

OpenCores.org host the source code for different digital HW projects (IP-cores, SoC, boards, etc) and support the users with different tools, platforms, forums and other useful information. Please join us!

### Projects



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### Professional support



**Professional support for OpenCores technology based products.**

If you plan to use IPs from OpenCores in your next design and need support, please contact ORSoC AB.

They are experts in SoC design based on the OpenCores technology and have extensive experience all parts of FPGA/ASIC development.

Please contact [ORSoC](#) for further information.

### Registered OpenCores users



**274470**

[OpenCores statistics](#)

### Last updated projects

- [Reed Solomon Codec](#)
- [Boost Converter](#)
- [Yet Another Hamming Encoder and Decoder](#)
- [Double Clocked FFT Core](#)
- [Another Wishbone Controlled UART](#)
- [OpenArty](#)
- [Wishbone Scope](#)
- [XuLA2-LX25 SoC](#)



### Most popular projects

- [OpenRISC 1000](#)
- [minsoc](#)
- [I2C controller core](#)
- [SPI controller core](#)
- [SPI Master/Slave Interface](#)
- [Alwcpu - A light weight CPU](#)
- [SPI Verilog Master & Slave modules](#)
- [openGFX430](#)



### News

# OpenCores.org

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- A portal that since 2003 collected >250.000 professionals in the field of gateware IPs
- Generates ~500.000 views per month
- Strong identity, established trademark, consistent community, very specialized, potentially business oriented
- Used to be maintained by a Swedish company
- Unkempt since 2012, but it is still the largest community for such niche market

# Oliscience goals

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- *Open Logic Interconnects science*
- **Acquired** the portal
- **Activate** the community
- **Offer** an “impact metrics” to asses performance and motivate designers to contribute (for **labs**)
- **Propose** “hardware platform matching” as a sort of advertisement opportunity (for **industry**)
- **Provide** consultancy services via the portal in the field of gateway design and support

# Why bother?

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- For **designers**: engaging experience encourages to contribute and participate
- For **labs**: showcase their capabilities, find new partnerships, approach funding agencies
- For **companies**: potential market place popular among users in the field
- For all: it's a neutral and healthy place to meet and work in synergy
- For all: let professionals take care of the infrastructure

# Planning for this year

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- **Taken** ownership of the website
- **Found** help to get my ideas in motion: contacted the Nikhef-CERN (BIC) Business Incubator Centre
- **Probe** the community for changes
- **Search** for partners especially in the scientific community
- **Develop** business model and marketing strategy



# Yes, I will “valorize”! 8)

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## What is valorisation?

**Valorisation is defined and applied in various ways.**

Policymakers like the VSNU, Rathenau, Advisory Council for Science, Technology and Innovation (AWT), and NWO define ‘valorisation’ as follows:

**‘Valorisation is the process of creating value from knowledge, by making knowledge suitable and available for societal and/or economic application and by transforming it into products, services, processes and new business.’**

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