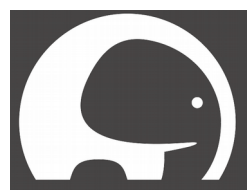




oliscience

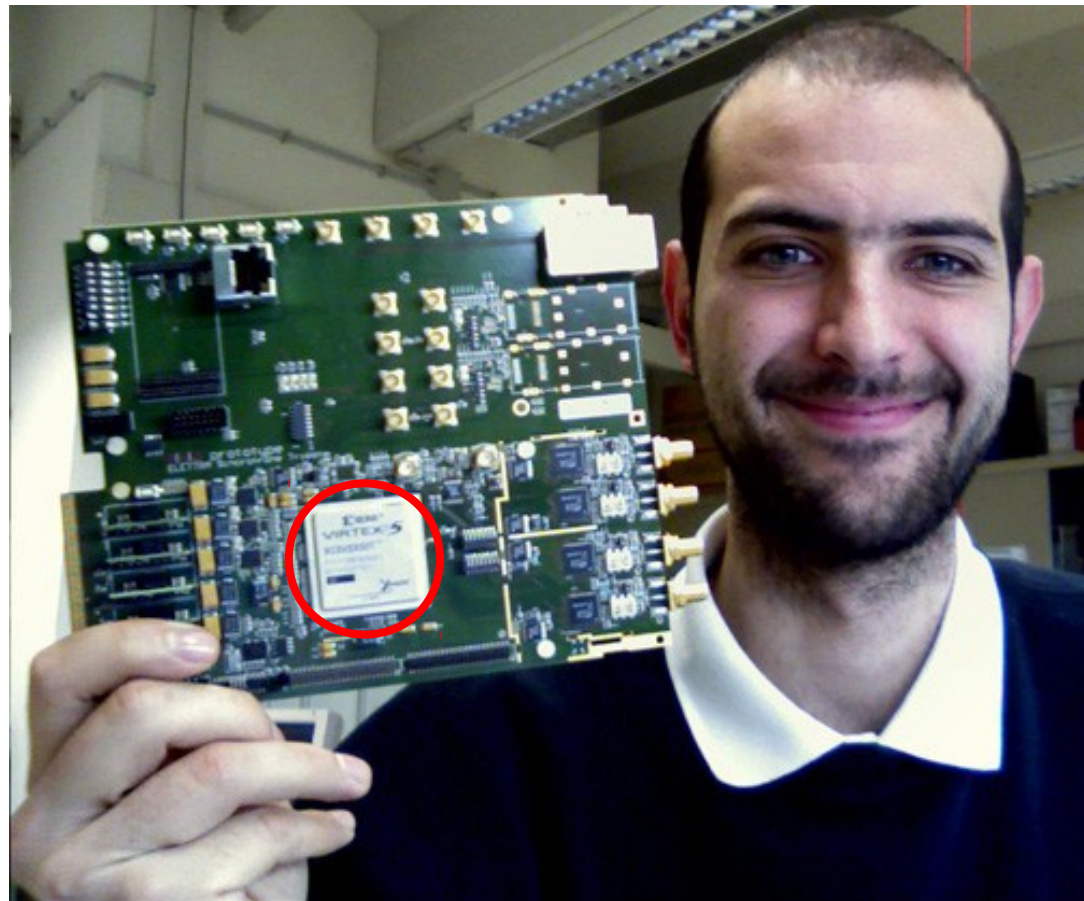
open logic interconnects science

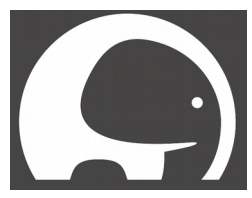
Andrea Borga (Digital Design Engineer / co-founder)



FPGA

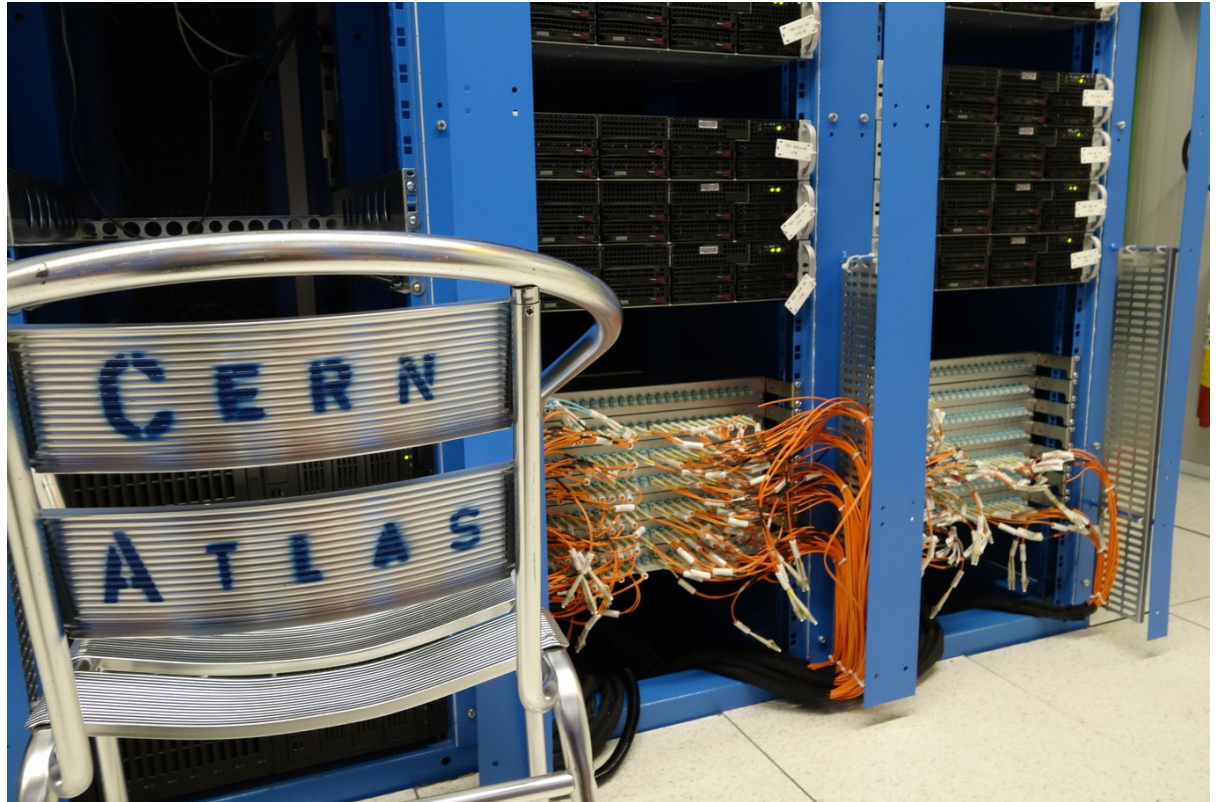
components widely used in high-end technology markets

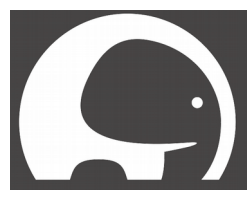




Examples of FPGA customers

- Science

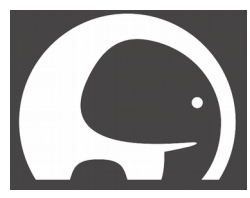




Examples of FPGA customers

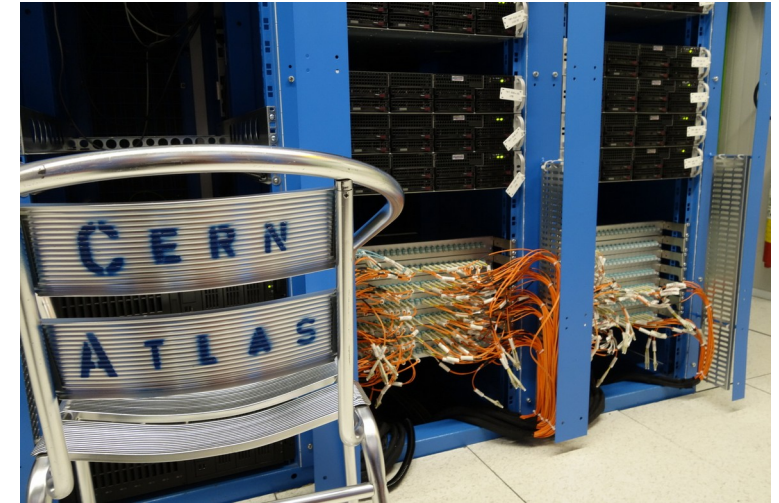
- Science
- Big data





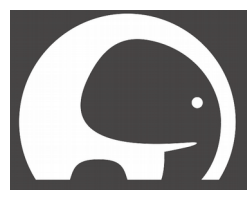
Examples of FPGA customers

- Science
- Big data
- Security



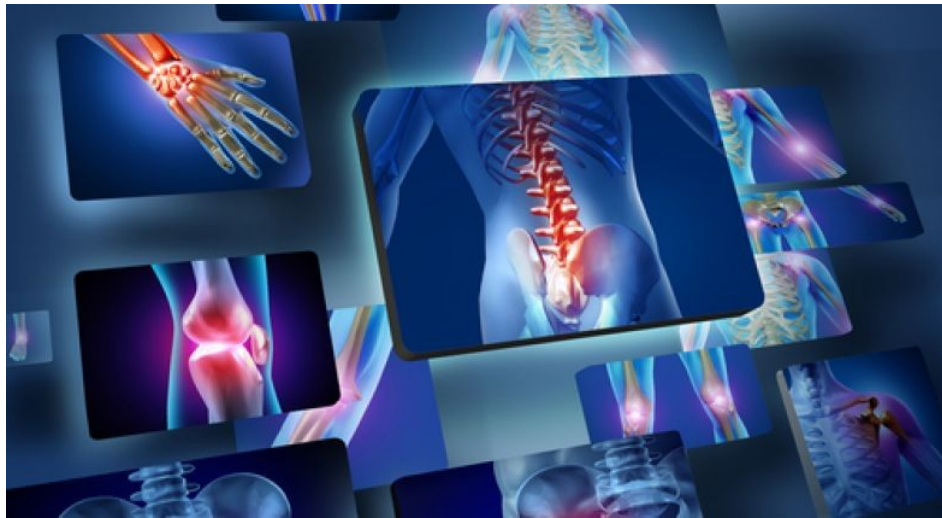
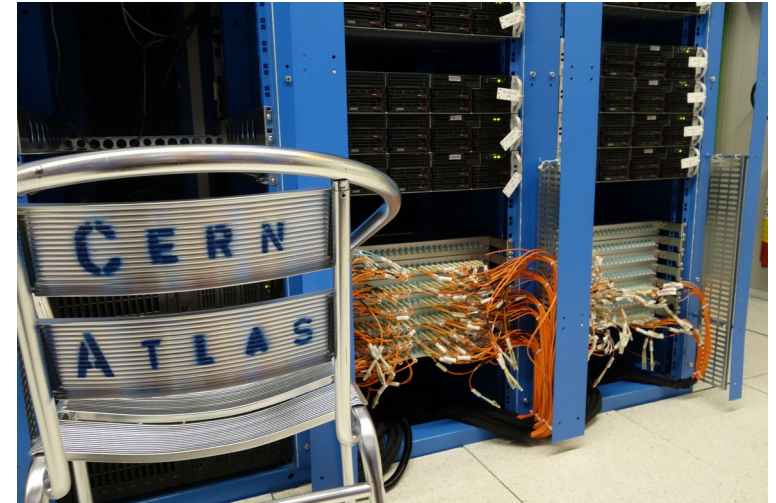
Accelize

amazon
web services



Examples of FPGA customers

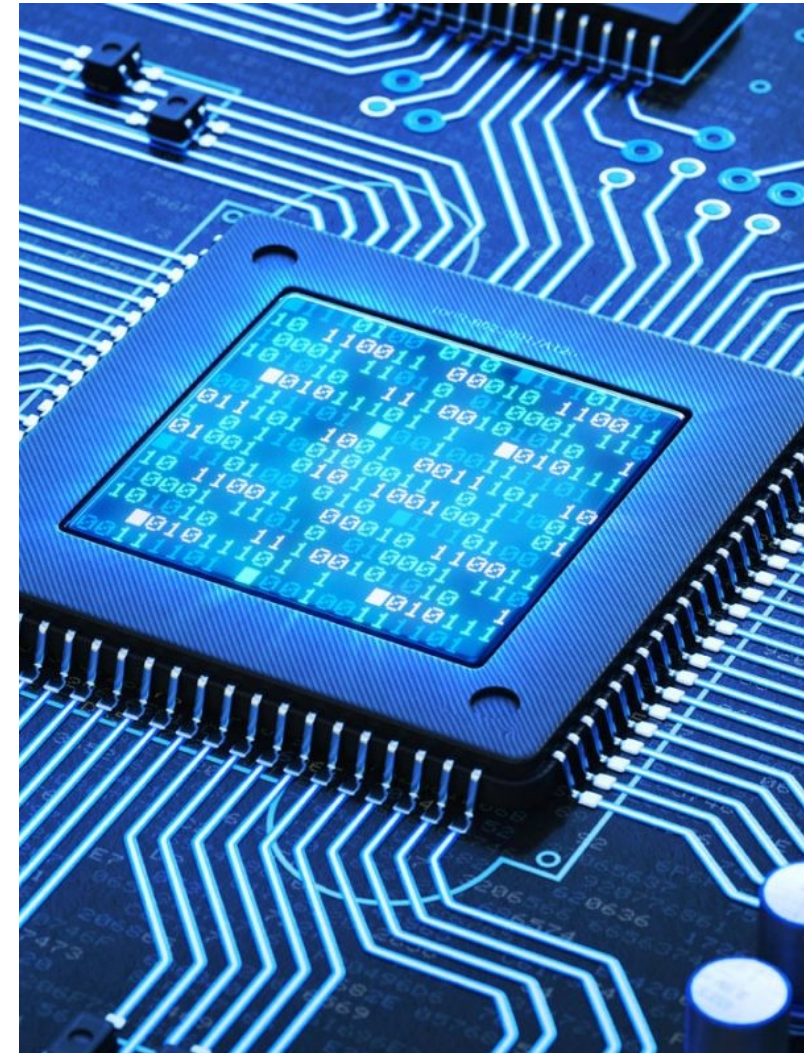
- Science
- Big data
- Security
- Medical imaging

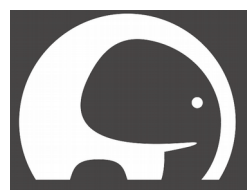




Gateway

“Intelligence” inside the FPGA



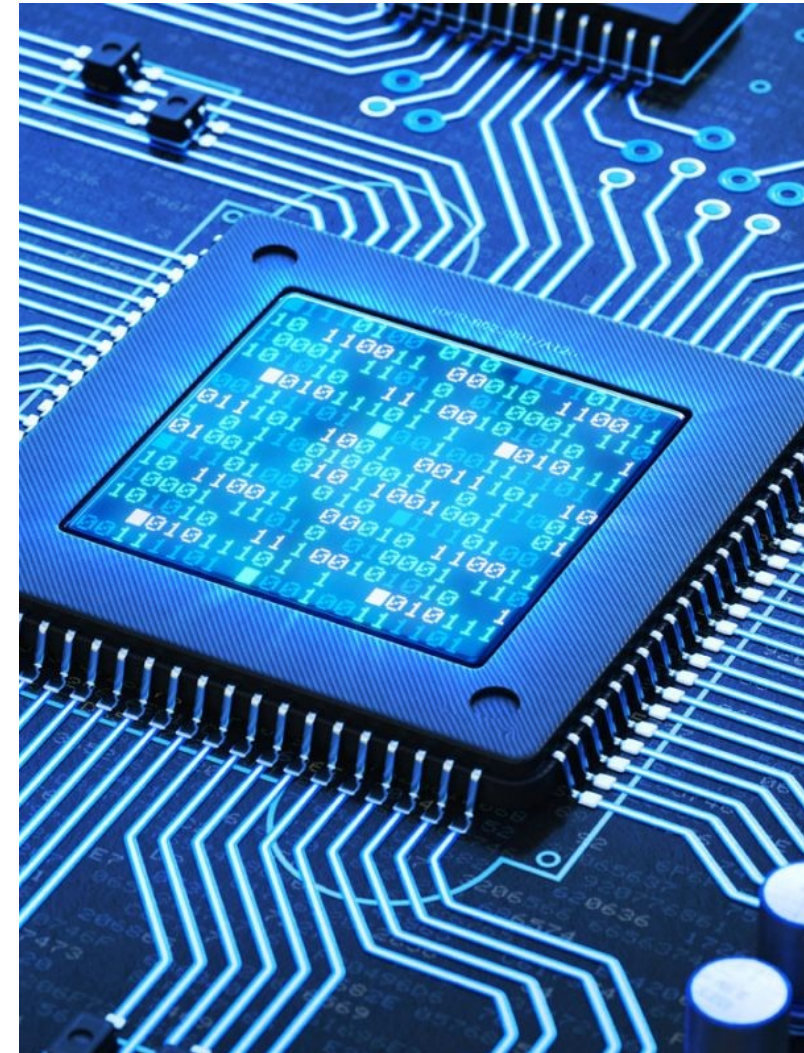


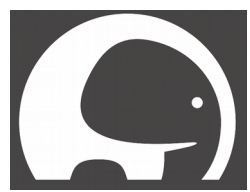
Gateway

“Intelligence” inside the FPGA



**Only a few professionals can handle
(5% electrical engineers)**





The BIG needs

- Shortage of gateway developers
 - projects development heavy and costly
- Lack of information exchange
 - effort duplication

How can we solve these problems?



Bridging gap with Open Source

Home :: OpenCores - Chromium

Home :: OpenCores x oliscience - open lo x

Secure | <https://opencores.org>

**OpenCores**
www.opencores.org

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Tools

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What is OpenCores?



The reference community for Free and Open Source gateway IP cores

Since 1999, OpenCores is the most prominent online community for the development of gateway IP (Intellectual Properties) Cores. It is the place where such cores are shared and promoted in the spirit of Free and Open Source collaboration.

The OpenCores portal hosts the source code for different digital gateway projects and supports the users' community providing a platform for listing, presenting, and managing such projects; together with version control systems for sources management.

OpenCores is also the place where digital designers meet to showcase, promote, and talk about their passion and work. They do this through forums, news collectors, and much more!

Please join us!

Registered OpenCores users



284381

[OpenCores statistics](#)

Last updated projects

- [Register Oriented Instruction Sets](#)
- [LEM1_9](#)
- [USB 1.1 Function IP Core](#)
- [2's compliment adder subtractor](#)
- [SpaceWireSystemC](#)
- [ODESS Multicore Project](#)
- [Video Stream Scaler](#)
- [Amber ARM-compatible core](#)

Most popular projects

- [CF Reconfigurable Computing Array](#)
- [I2C controller core](#)
- [Hilbert Transformer](#)
- [SPI Master/Slave Interface](#)
- [Raggedstone PCI Spartan-3 board](#)
- [SPI Verilog Master & Slave modules](#)
- [double_fpu_verilog](#)
- [I2C master/slave Core](#)

Projects



[Browse all Projects \(Cores\)](#)

Forum



[Communicate in the forums](#)

WebShop



[Visit our Webshop](#)

Professional support

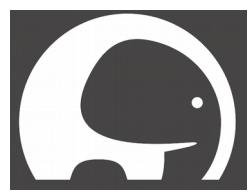


We are the developers and maintainers of this website and community, but not only!

If you plan to use IP Cores from OpenCores in your next design and need support, or if you require professional advise on your next challenging IP Core development, don't hesitate to contact us.

We are experts in gateway design and engineering based on the OpenCores technology, and have extensive experience in all parts of FPGA development.

Please visit [Oliscience](#) for further information and enquiries.

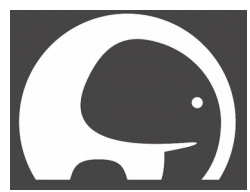


Our customers

Whoever pays costs for gateway development

- Research institutions
- High tech. corporates

Engage them on OpenCores

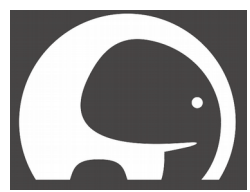


The solution: OpenCores

**Providing customers a special role in the
reference community of
~300.000 highly specialized users**

Benefits:

- Showcase own development and skills
- Knowledge access to more than 1000 IP Cores
- Engage in the community to promote trusted and certified contributions



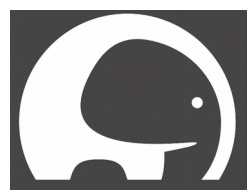
Competitive analysis

LibreCores

- Less than 100 IP Cores
- Undeclared number of users

OHWR

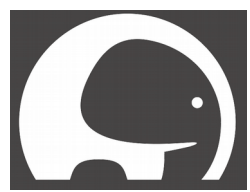
- Generic open “hardware” portal for sharing various projects
- Gateware section: estimated 100 IP Cores



Business model

Phase I

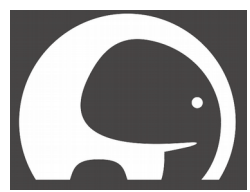
- Action: sell memberships plans for customers like research institutions and corporates → in exchange of higher visibility and ad-hoc support services
- Action: improve the user experience and the overall website
- Goal: Enhance users' engagement and strengthen the community



Business model

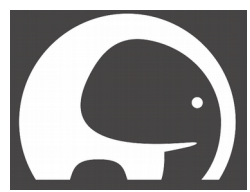
Phase II

- Provide customers and users with
 - devices (hardware platforms, verification and integration tools)
 - consultancy services (methodologies, IP Cores streamlining and data quality assessment, long term support)
- Goal: be the go to company for gateway developments



Go to Market

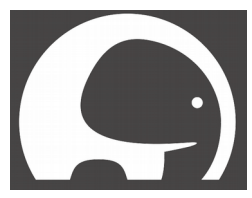
- Customer product adoption
- Strong partnership with Nikhef
→ other research institutes join forces
- Leverage on the community itself
(already received expressions of interest by potential customers)



Traction

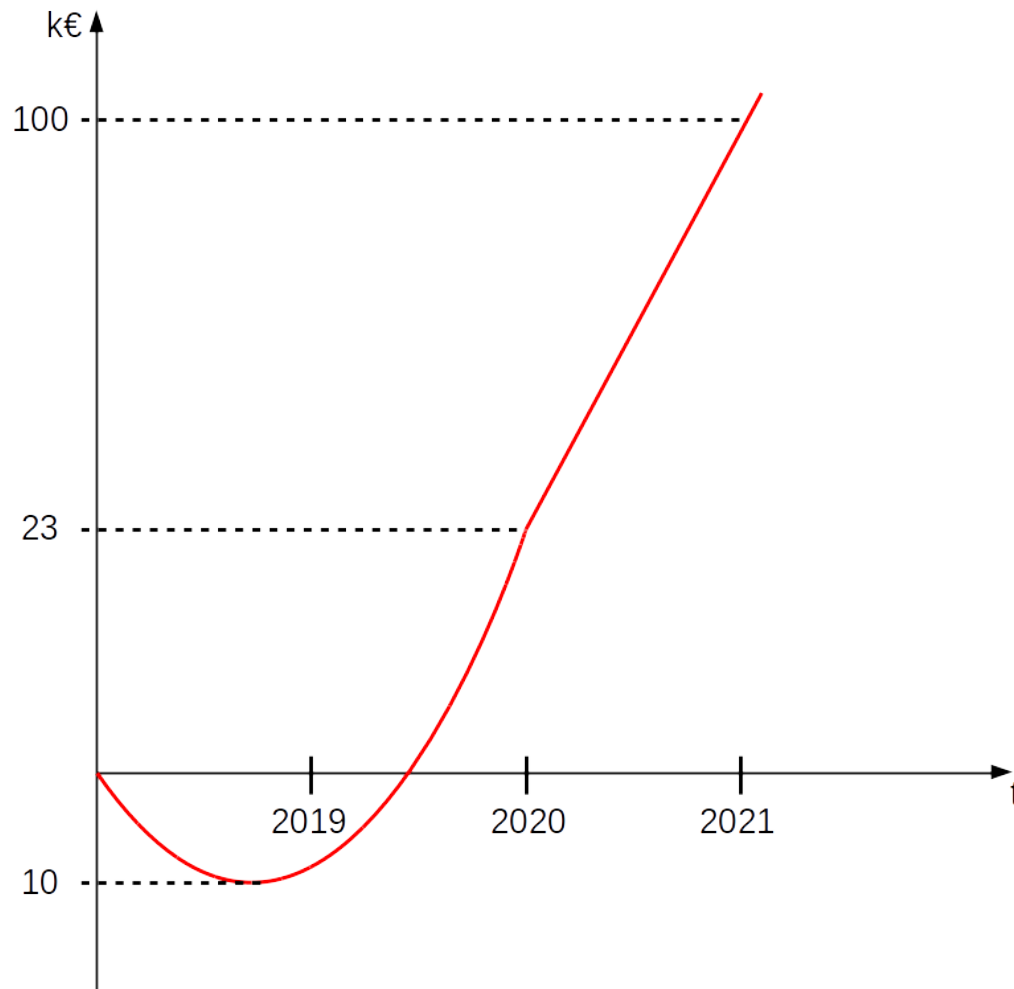
Grow trust in Oliscience

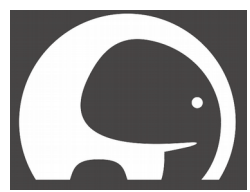
- Partnership with research institutes
 - Nikhef
 - ASTRON
- Validating Oliscience with
 - other labs
 - users



Financial projections

- Revenues and costs

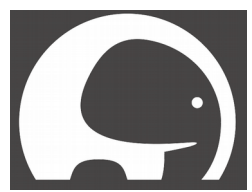




Looking for?

Grow trust in Oliscience

- Validation of our concept
 - Acquire paying members
- Turbo-boost the business
 - Marketing is key
- Get expert feedback



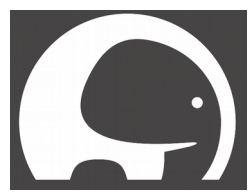
Conclusion

Open Source worlds have

Software → Linux

Hardware → Arduino

Gateway → ?



Oli-team





Image credits

- **Slides 3-6**

Andrea Borga (for <http://atlas.cern/>)

<https://www.camoni.co.il/> כל-המחלות-הנלוות-לדלקות-מפרקים-אוטואימוניות

<https://newsroom.intel.com/editorials/cpus-fpgas-software-come-together-ces-bolster-automated-driving/>

<https://www.accelize.com/news/accelize-integrates-amazon-ec2-f1-instance-its-quickplayquickstore-framework?page=3>

- **Slide 7**

Oliscience.nl

Random google search “human brain”